

FIG. 1

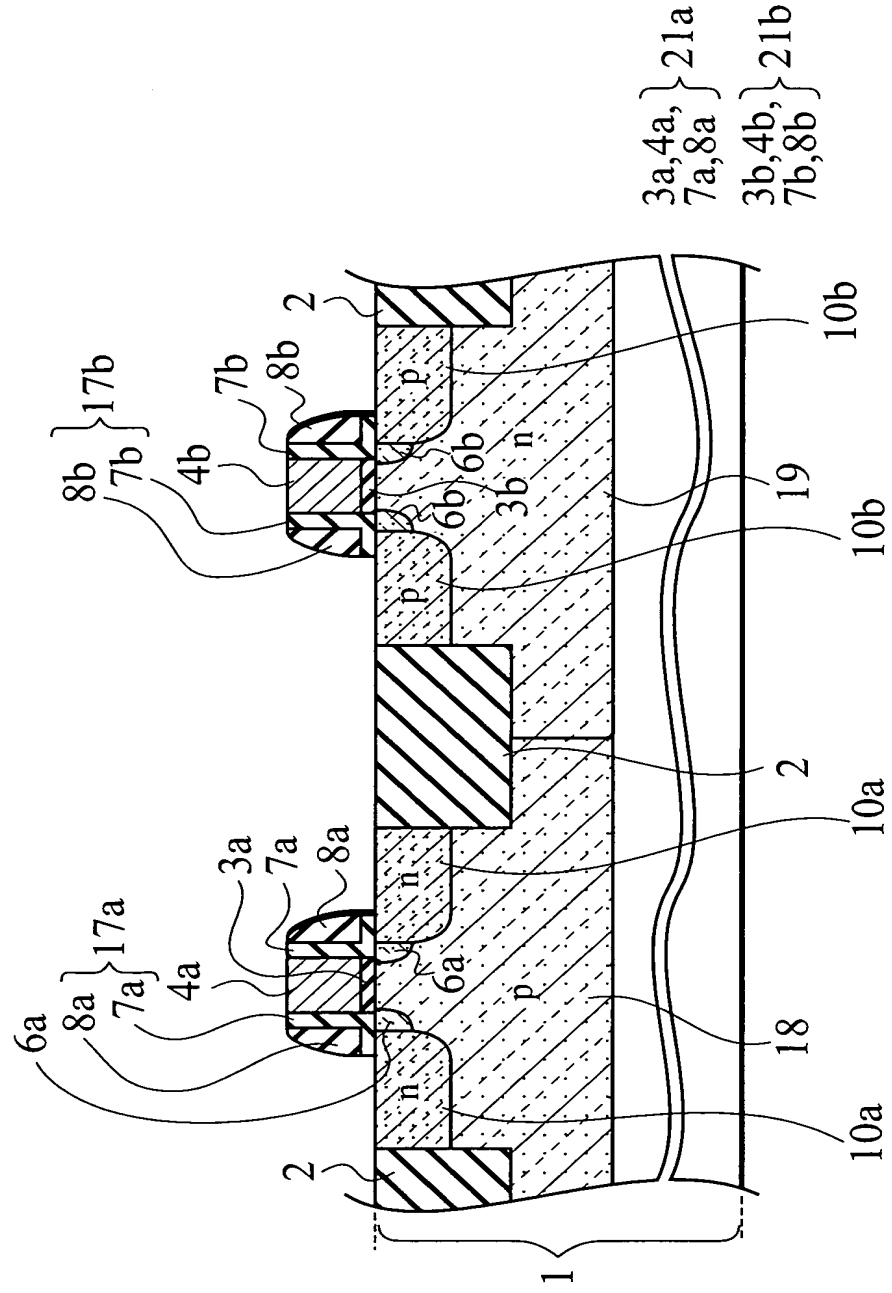


FIG. 2A

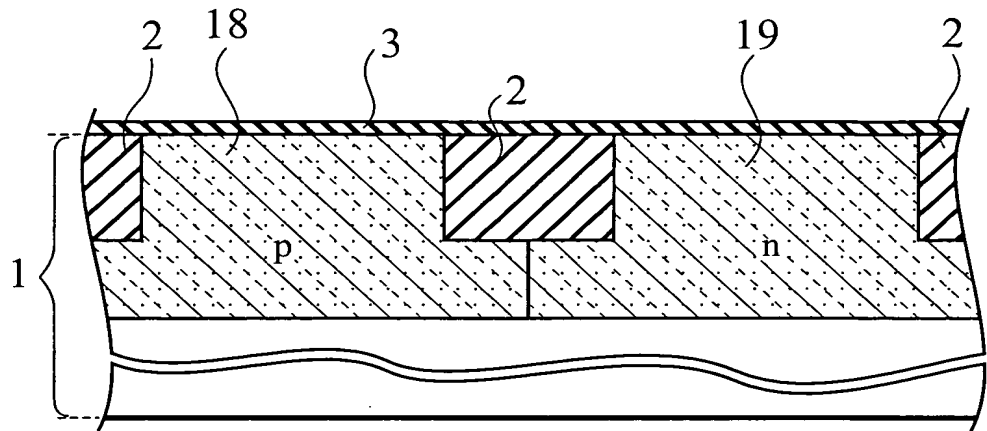


FIG. 2B

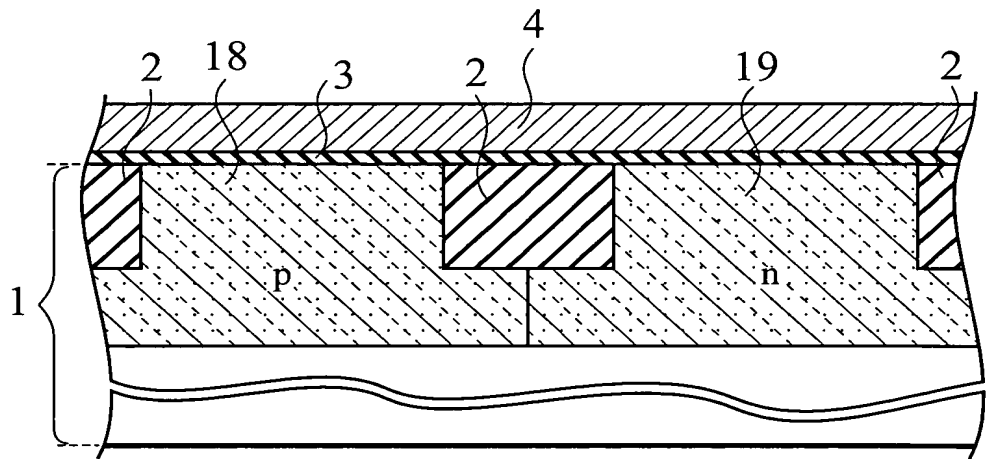


FIG. 2C

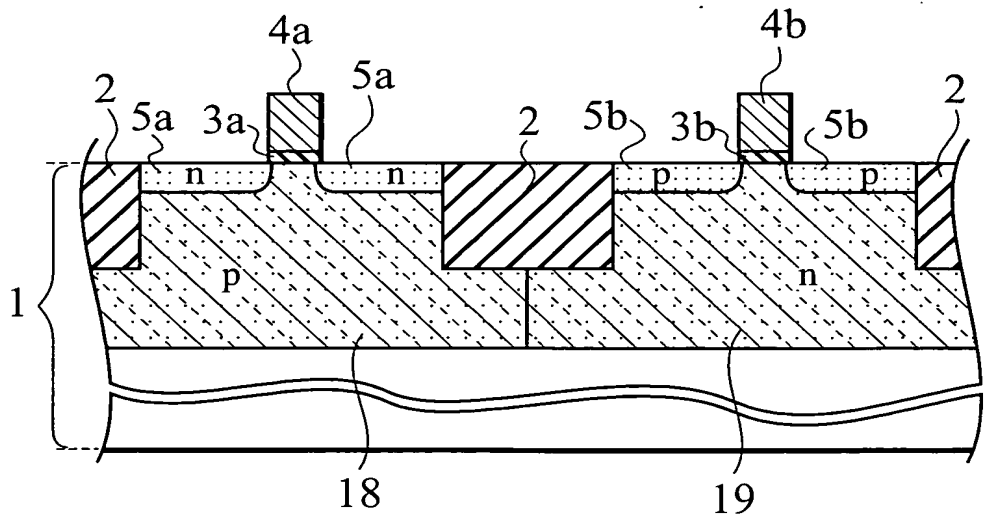


FIG. 3B

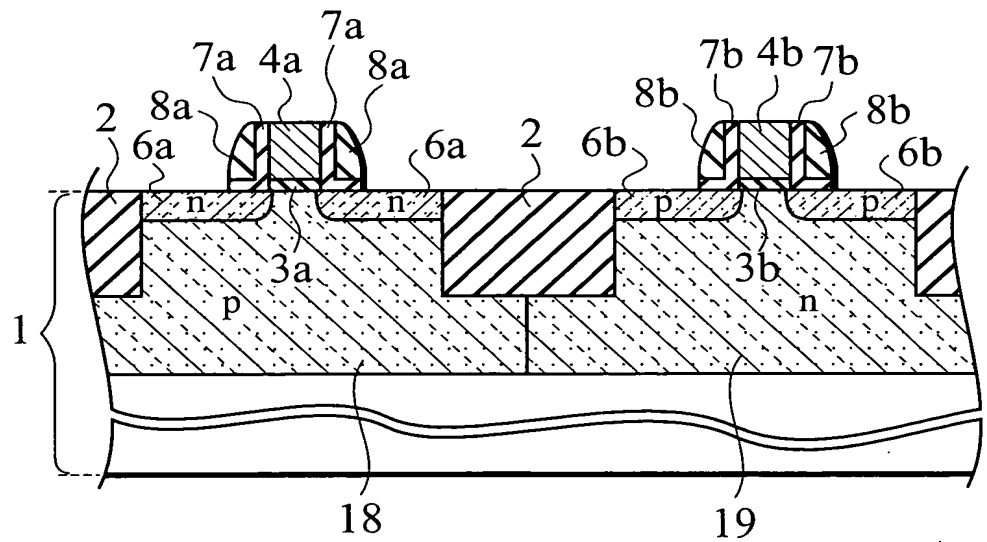

$$\left. \begin{matrix} 7b, \\ 8b \end{matrix} \right\} 17b$$

FIG. 4A

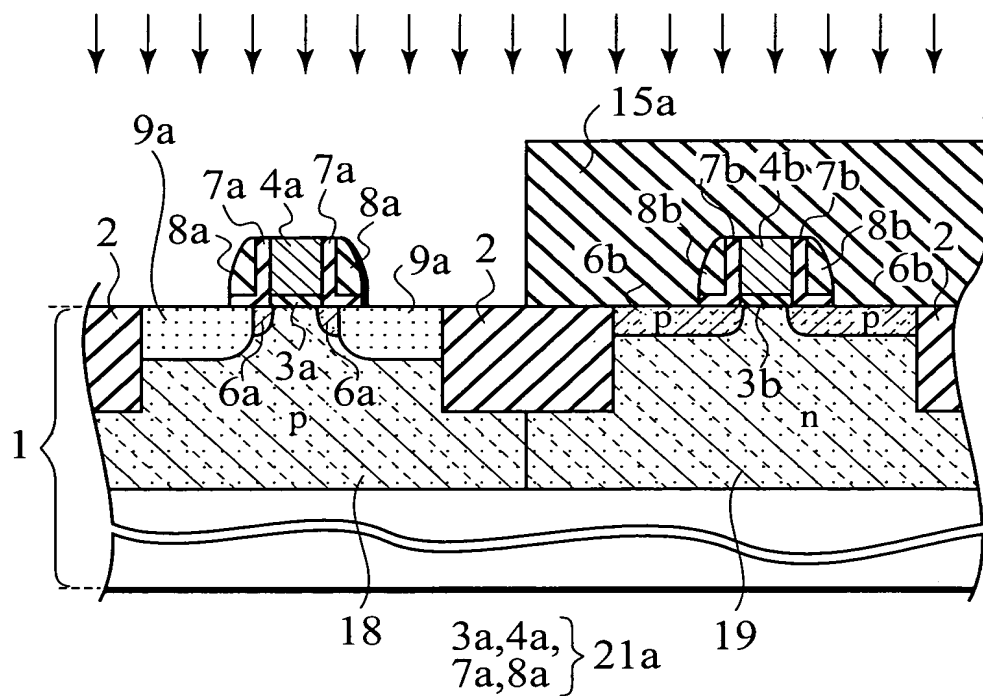


FIG. 4B

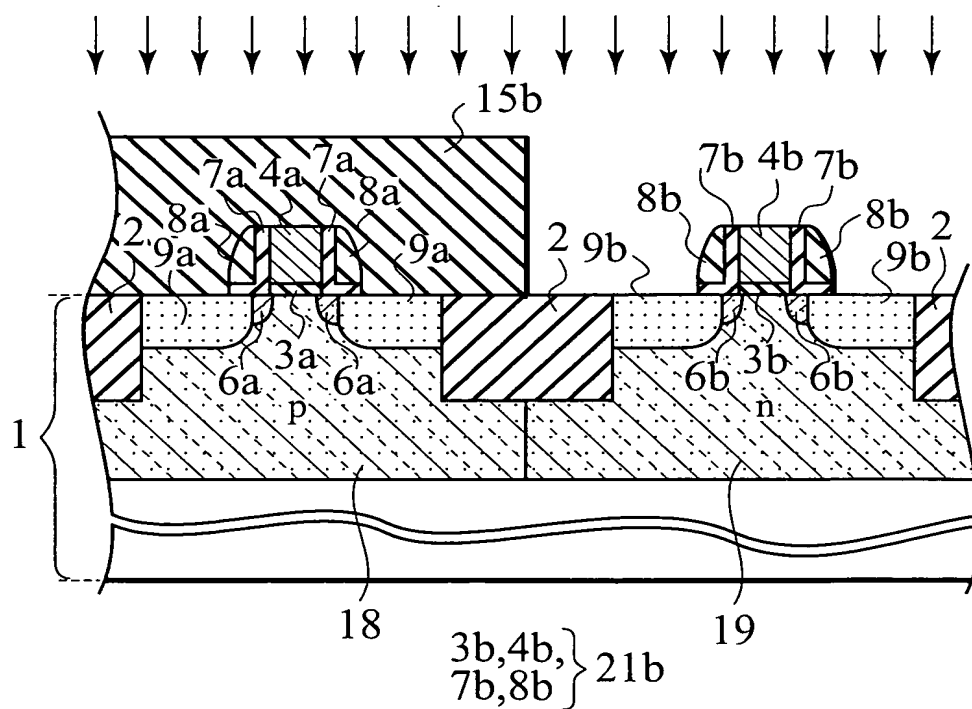


FIG. 5

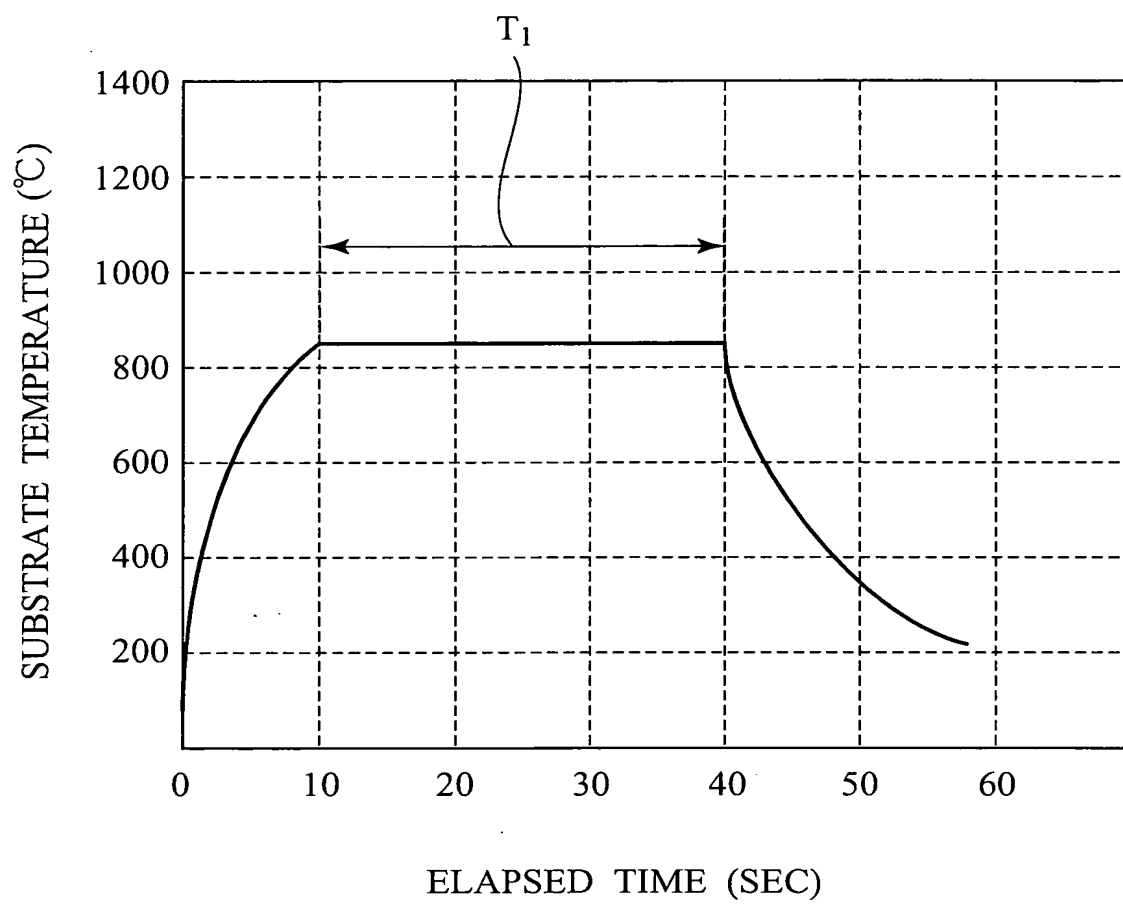


FIG. 6

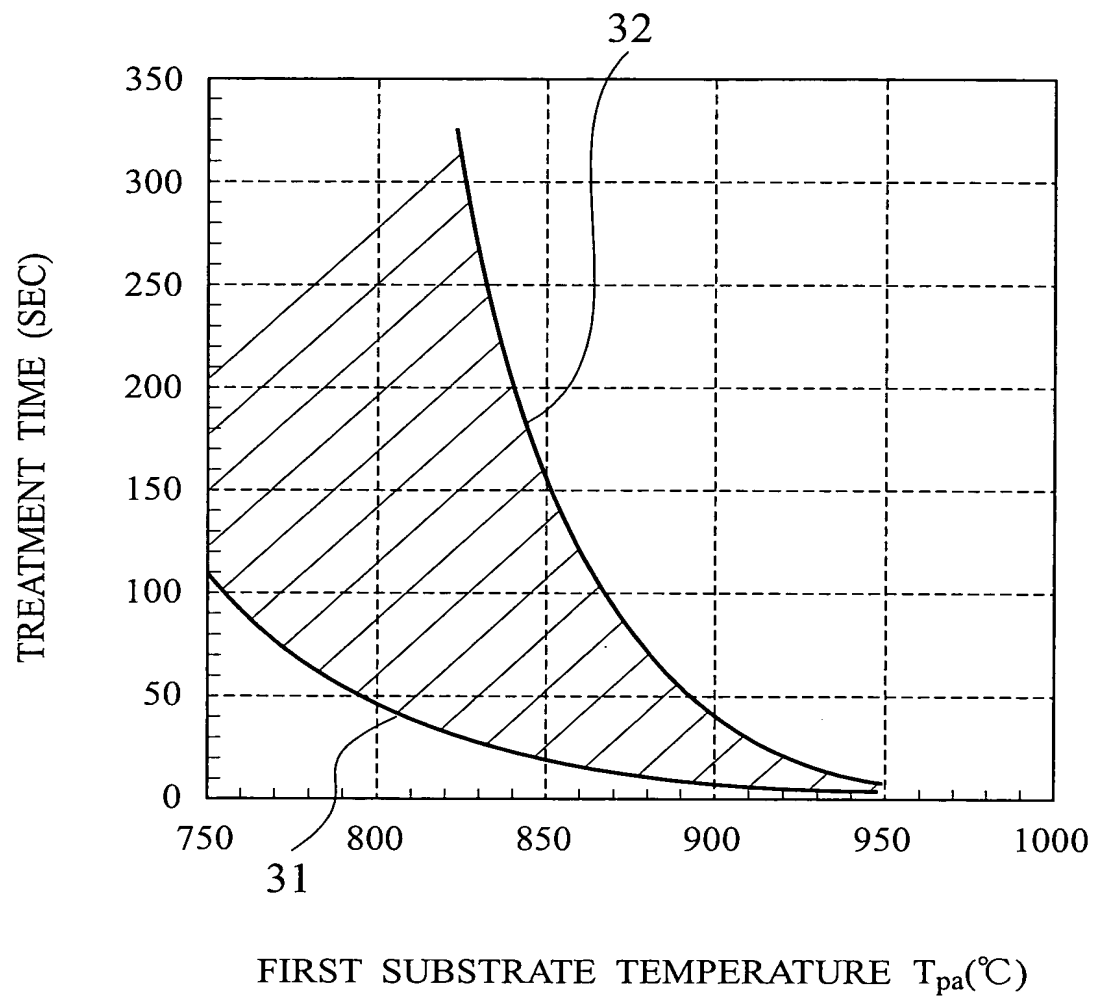


FIG. 7

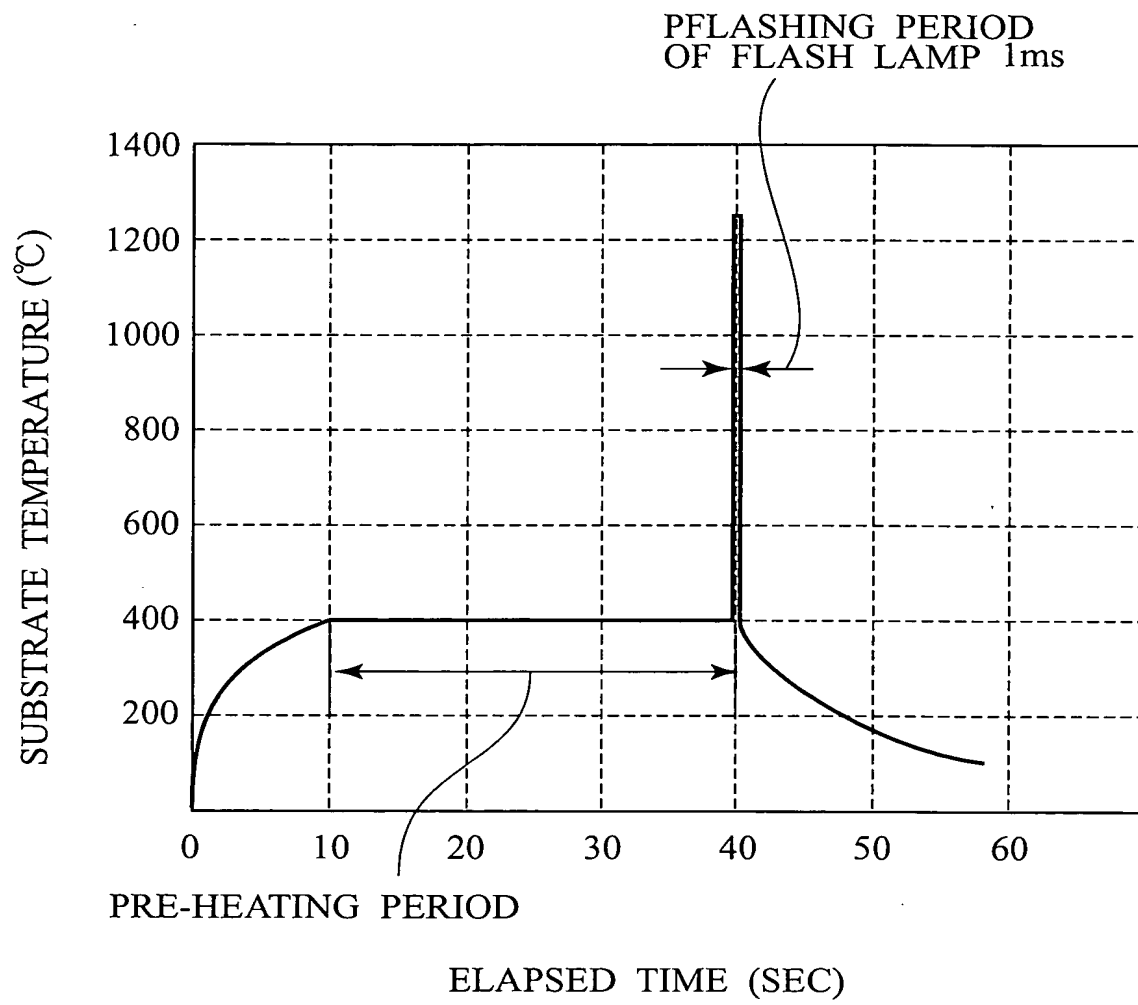


FIG. 8A

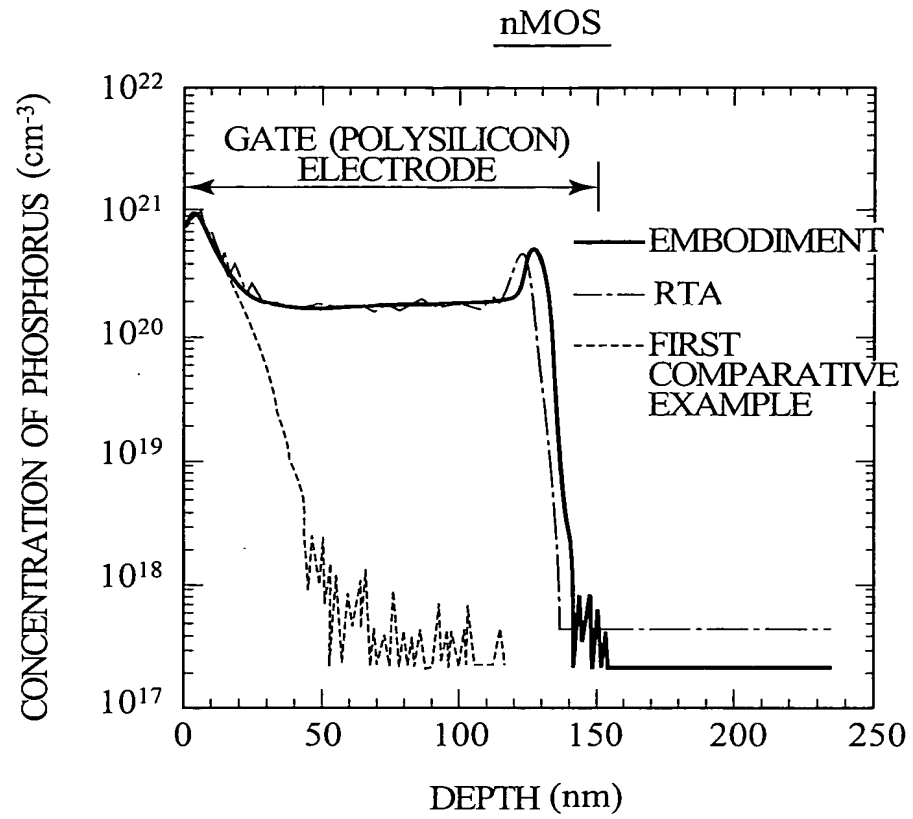
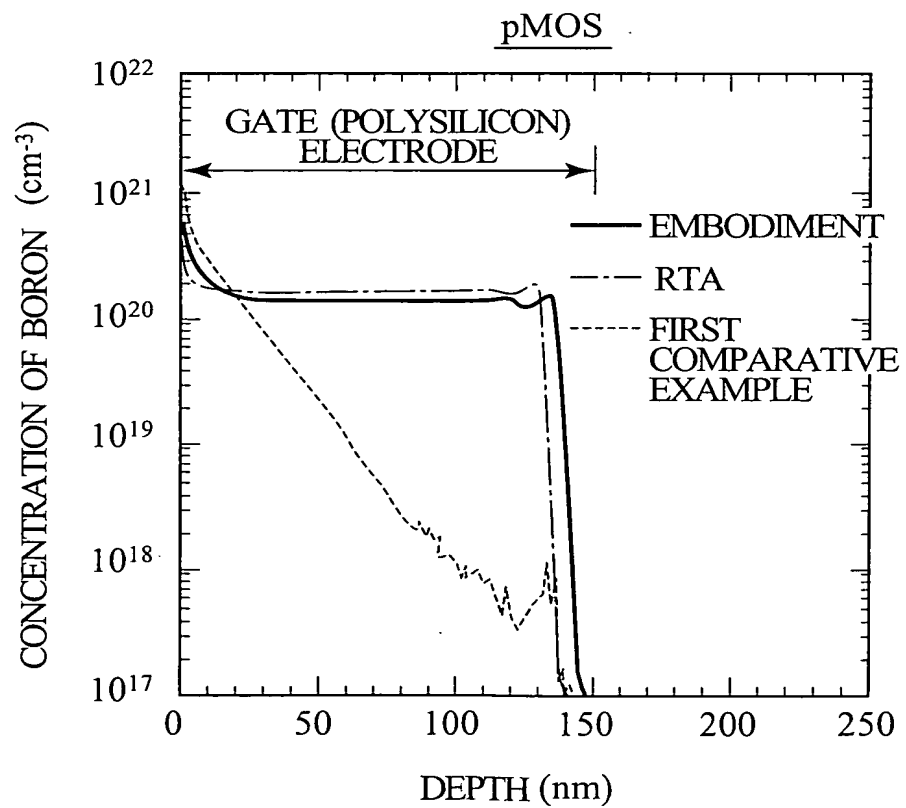


FIG. 8B



nMOS

FIG. 9A

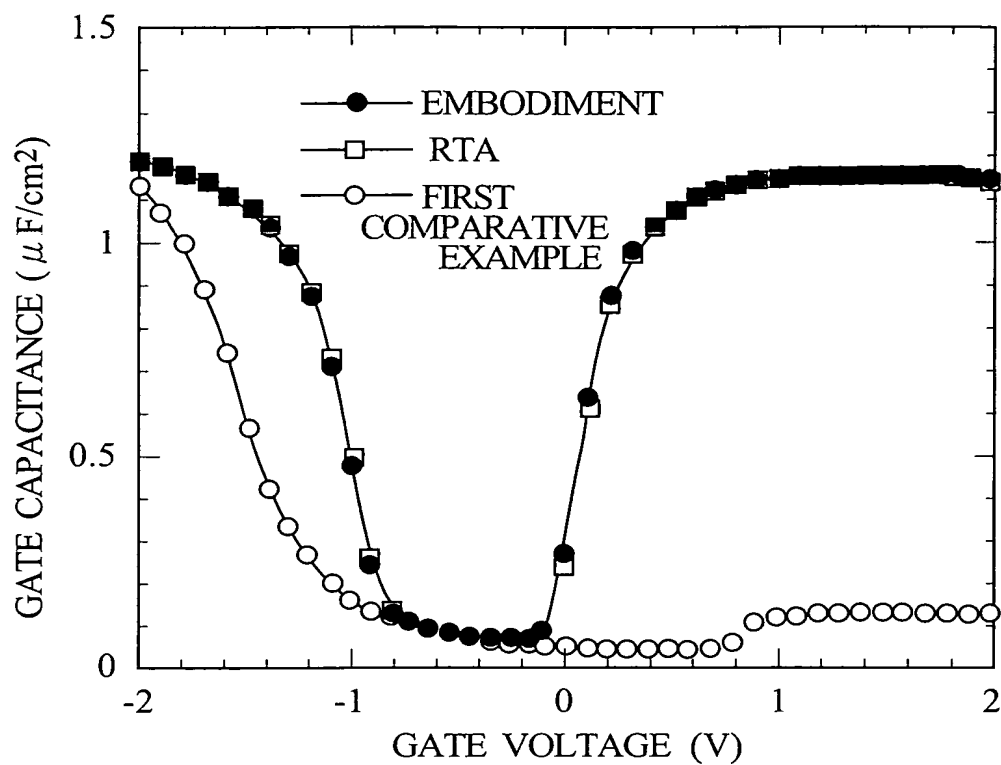
pMOS

FIG. 9B

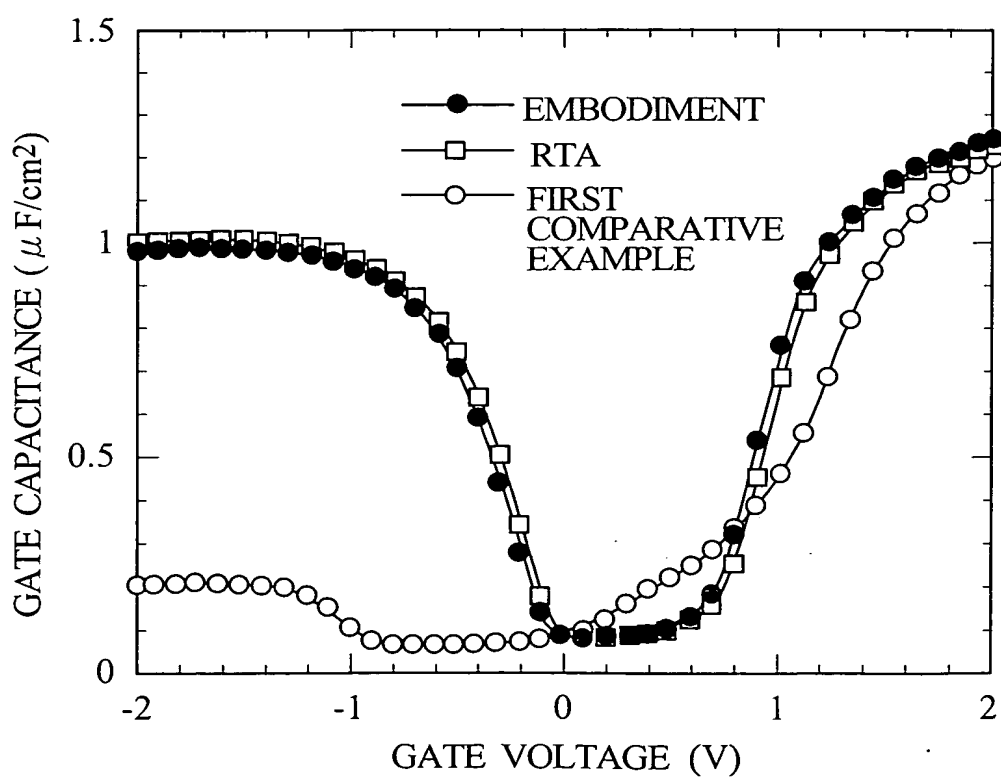


FIG. 10A

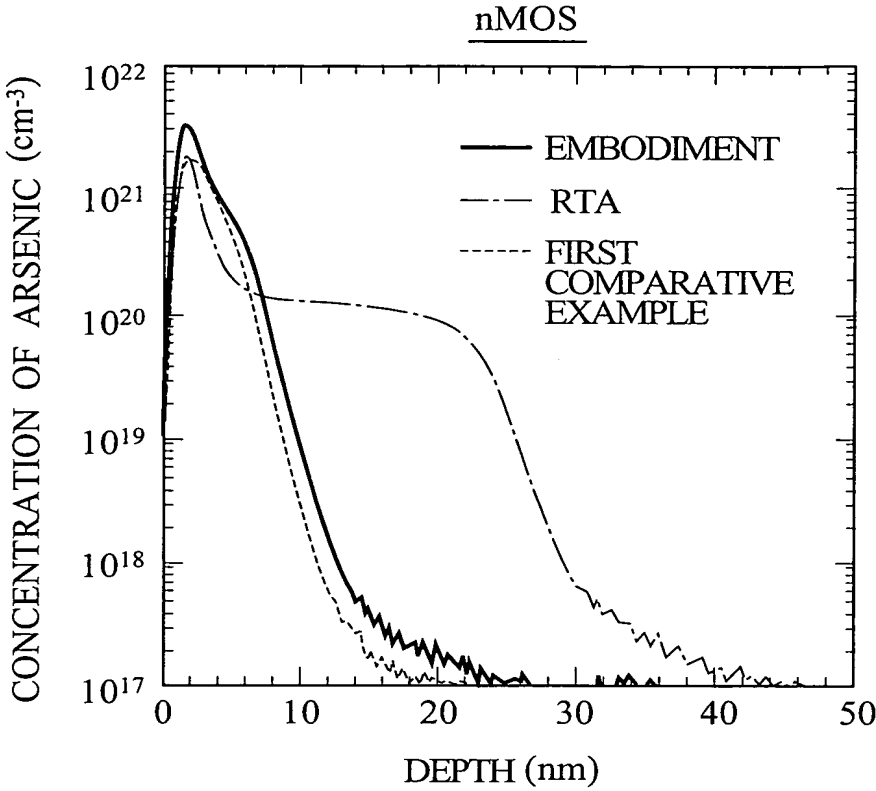


FIG. 10B

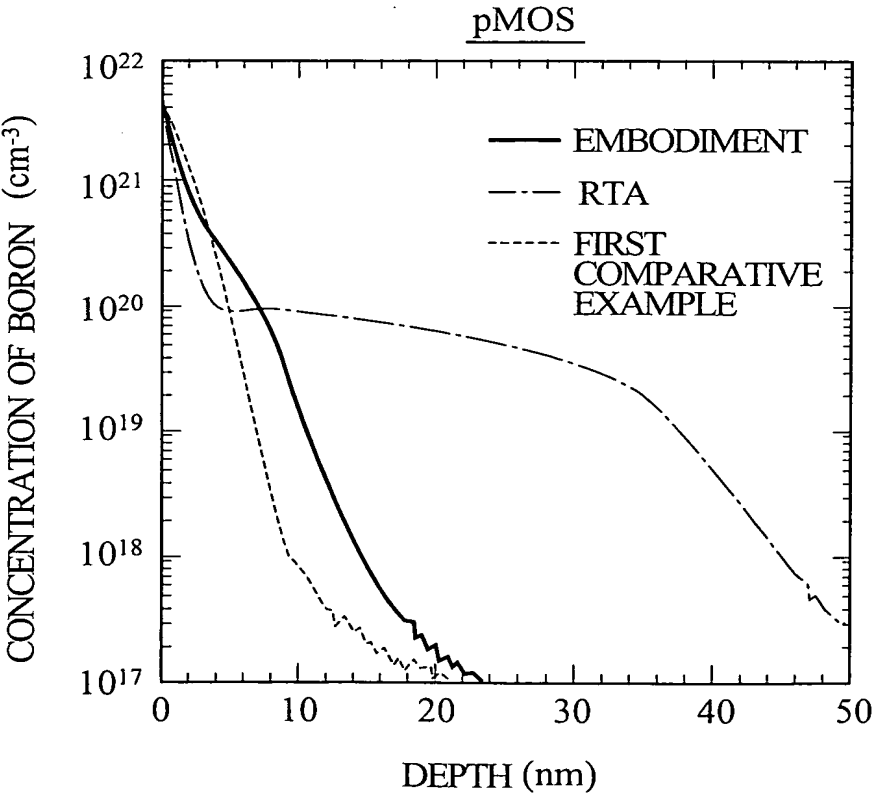


FIG. 11A

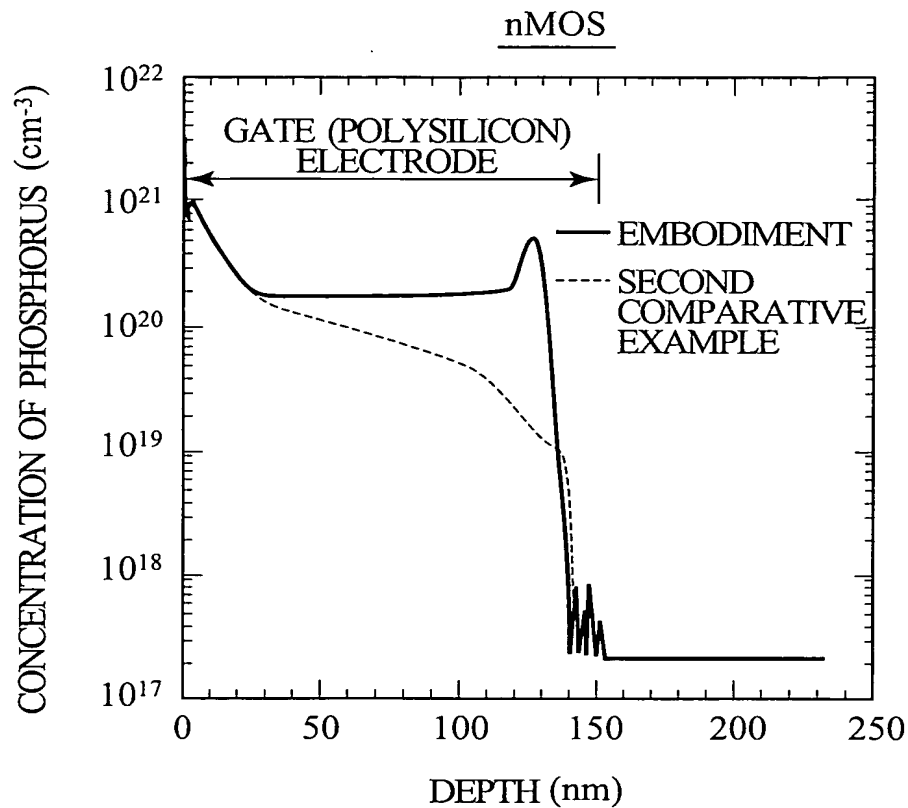
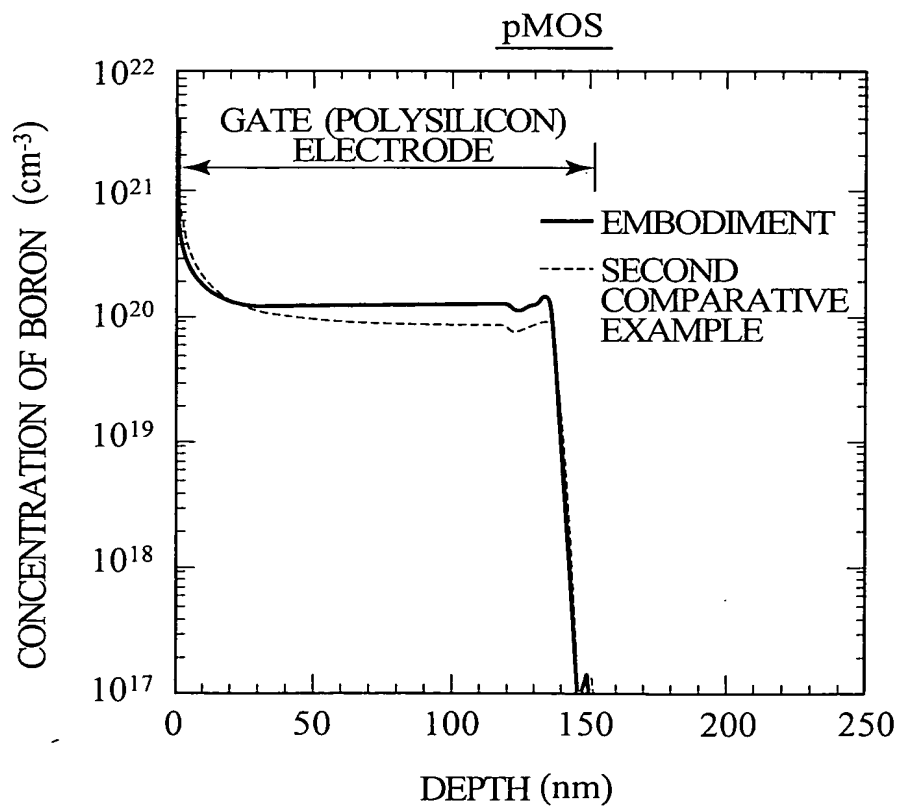


FIG. 11B



nMOS

FIG. 12A

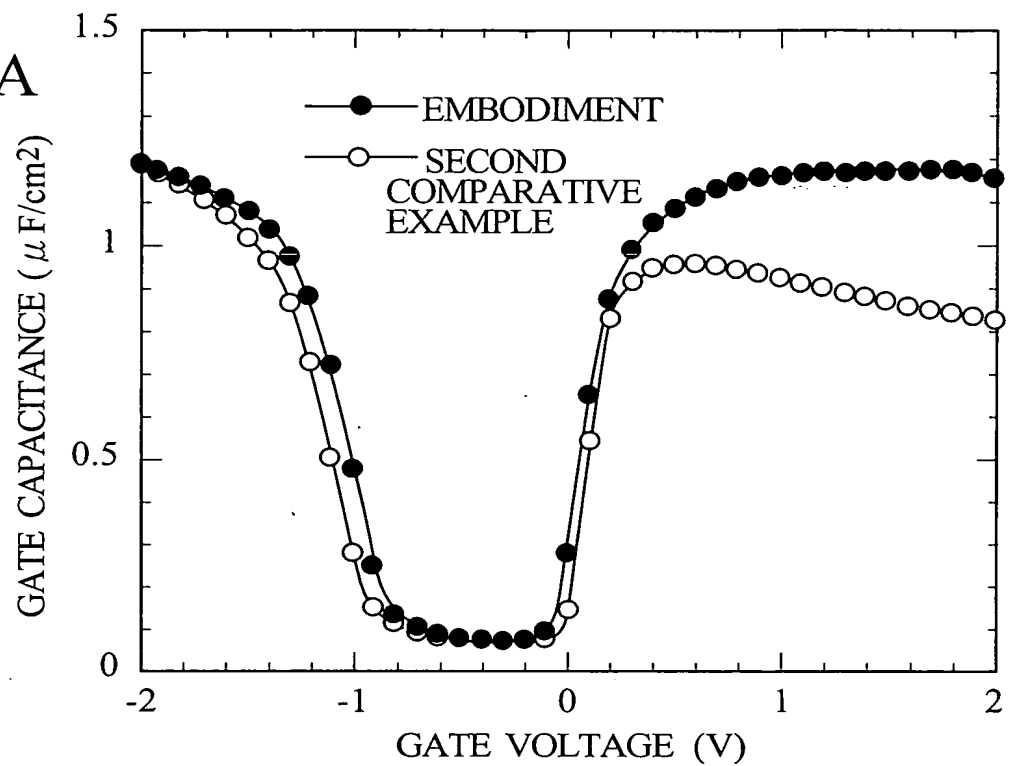
pMOS

FIG. 12B

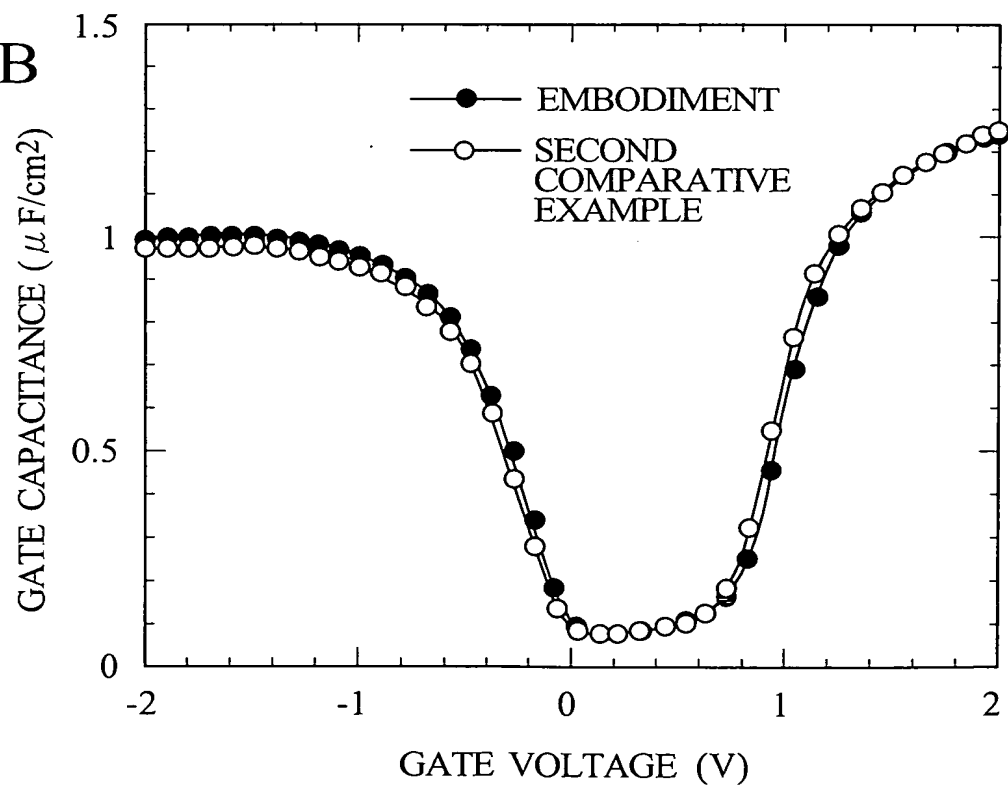


FIG. 13

